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Irena NikolovaIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: January 5, 2005

Yuji Okuda

Confirmation No.: 1643

Serial No: 10/018,708

Group Art Unit: 3723

Filed: April 15, 2002

Examiner: Wilson, Lee D

For: TABLE OF WAFER POLISHING APPARATUS, METHOD FOR POLISHING
SEMICONDUCTOR WAFER, AND METHOD FOR MANUFACTURING
SEMICONDUCTOR WAFER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action dated September 22, 2004, please amend the above-
identified application in the following manner:

Amendments to the Claims are reflected in the listing of claims which begins on page 2
of this paper.

Remarks begin on page 9 of this paper.

01/11/2005 SSITHIB1 00000019 10018708

01 FC:1251 120.00 OP

01/11/2005 SSITHIB1 00000019 10018708

02 FC:1202 250.00 OP